

**INTEGRATED TREATMENT METHOD FOR
OBTAINING ROBUST LOW DIELECTRIC CONSTANT MATERIALS**

Inventors: Mong-Song Liang
2F, No. 12, Bamboo Road 7
Science-Based Industrial Park
Hsin-Chu, Taiwan 300, Republic of China
Citizenship: Taiwan, R.O.C.

Yung-Cheng Lu
No. 8, Li-Hsin Rd. 6
Science-Based Industrial Park
Hsin-Chu, Taiwan 300, Republic of China
Citizenship: Taiwan, R.O.C.

Huilin Chang
50 Ta-Hsueh Road, 10F-4
Hsin-Chu, Taiwan 300-50, Republic of China
Citizenship: Taiwan, R.O.C.

Assignee: Taiwan Semiconductor Manufacturing Co., Ltd.
No. 8, Li-Hsin Rd. 6, Science-Based Industrial Park
Hsin-Chu, Taiwan, R.O.C. 300-77

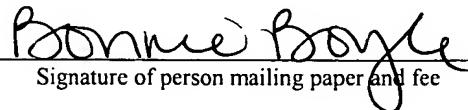
HAYNES AND BOONE, L.L.P.
901 Main Street, Suite 3100
Dallas, Texas 75202-3789
(214) 651-5000
(214) 200-0853 - Fax
Client Reference No.: 2003-1506
Attorney Docket No. 24061.197
Document No.: R-67292.1

EXPRESS MAIL NO.: EV 333440414 US DATE OF DEPOSIT: March 26, 2004

This paper and fee are being deposited with the U.S. Postal Service Express Mail Post Office to Addressee service under 37 CFR §1.10 on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Bonnie Boyle

Name of person mailing paper and fee


Signature of person mailing paper and fee

INTEGRATED TREATMENT METHOD FOR OBTAINING ROBUST LOW DIELECTRIC CONSTANT MATERIALS

BACKGROUND

[0001] In the field of semiconductor device fabrication, inter-level dielectric (ILD) materials are used to separate conductive elements such as metal interconnect lines. As device sizes shrink, the smaller spacing between these conductive elements leads to increased parasitic capacitance that causes problems such as delays in signal transmission. Low dielectric constant (k) materials are desirable for use as inter-level dielectrics to minimize the parasitic capacitance. Because air has the lowest dielectric constant of one, materials that are porous have a low dielectric constant. Numerous fabrication processes have heretofore been used to produce porous low dielectric materials. However, the resultant low dielectric materials are generally fragile, soft, and mechanically unstable. These materials are often unable to withstand the mechanical and/or chemical processes that follow in the fabrication process.

BRIEF DESCRIPTION OF THE DRAWINGS

[0002] FIGURE 1 is a simplified flowchart of an integrated post-deposition treatment method;

[0003] FIGURE 2 is a simplified flowchart of an integrated post-deposition treatment method according to an embodiment of the invention;

[0004] FIGURE 3 is a simplified flowchart of an integrated post-deposition treatment method according to another embodiment of the invention;

[0005] FIGURE 4 is a simplified flowchart of an integrated post-deposition treatment method according to another embodiment of the invention;

[0006] FIGURE 5 is a simplified flowchart of an integrated post-deposition treatment method according to another embodiment of the invention;

[0007] FIGURE 6 is a simplified flowchart of an integrated post-deposition treatment method according to another embodiment of the invention; and

[0008] FIGURE 7 is a simplified cross-sectional view of an exemplary semiconductor device.

DETAILED DESCRIPTION

[0009] Low dielectric constant (low k) materials are eagerly sought after for inter-level dielectric applications. The use of low dielectric materials decreases parasitic capacitances and cross-talk between neighboring conductors and improves electrical signal transmission. Traditional formation methods lead to a material that is soft, fragile with low cracking threshold, high film stress, chemical or thermal resistance, and/or other undesirable characteristics. Known low dielectric materials that may be used for inter-level dielectric applications include fluoride-doped silicate glass (FSG), BLACK DIAMOND (Applied Materials of Santa Clara, California), xerogel, aerogel, amorphous fluorinated carbon, parylene, bis-benzocyclobutenes (BCB), SILK (Dow Chemical of Midland, Michigan), and DENDRIGLASS (IBM of White Plains, New York).

[0010] Some low dielectric materials such as SILK and DENDRIGLASS are formed by the use of pore generators or porogen. During the formation of the dielectric material, the host material forms a matrix of cross links and the porogen separates from the matrix. Upon subsequent processing, the porogen is evacuated or removed and a network of pores is left behind.

[0011] FIGURE 1 is a simplified flowchart of one embodiment of an integrated post-deposition treatment method 10 for a low dielectric material 12. Low dielectric material 12 may comprise a film that has been deposited onto a semiconductor substrate by a number of methods

such as chemical vapor deposition (CVD), plasma-enhanced chemical vapor deposition (PECVD), spin-on deposition (SOD), etc. Integrated method 10 comprises treating low dielectric material 12 using a first post-deposition treatment 14 in combination with a second post-deposition treatment 16 to achieve a more mechanically stable material that has improved dielectric constant and other characteristics. For example, the resultant material may have increased hardness, lowered film stress and lower cracking threshold. Low dielectric materials produced in such a manner is better able to withstand subsequent processing steps such as chemical mechanical planarization (CMP) and still retain a low dielectric constant.

[0012] Depending on the application, first post-deposition treatment 14 may be performed prior to second post-deposition treatment 16, or the two treatment steps may be performed substantially simultaneously with the use of suitable equipment.

[0013] Subjecting low dielectric materials to various treatment methods have yielded a general finding of the results of these treatment methods:

	Thermal	Hydrogen Plasma	Microwave Hydrogen Plasma	Electron Beam	Ultraviolet Radiation
K Value	✓	✓	✓	-	-
Hardness	✗	✗	✓	✓	✓
Cracking Threshold	✓	✓	-	✗	✗
Chemistry/Thermal Resistance	✗	✗	✗	✓	✓

In the above table, the rows display material properties and the columns display treatment methods. The check marks, ✓, indicate that the treatment method improved that particular material property, the ✗ marks indicate that the treatment method worsened that particular material property, and the dash mark, -, indicate that the treatment method did not significantly alter that particular material property. It may be seen that, for example, thermal treatment alone

generally improved the dielectric constant and cracking threshold of the material, but made its hardness and chemistry/thermal resistance worse.

[0014] FIGURE 2 is a simplified flowchart of an integrated method 20 to stabilize a low dielectric material according to an embodiment of the invention. Method 20 is particularly suited for curing a non-porogen low dielectric material 22. A non-porogen material is one that does not rely on pore generators to form a porous network in the material. Non-porogen low dielectric material 22 is first subjected to a hydrogen plasma process 24, and then subjected to one of microwave hydrogen plasma, electron beam, or ultraviolet radiation processes 26. Hydrogen plasma process 24 comprises subjecting low dielectric material 22 to hydrogen plasma in a chamber for about 30 to about 600 seconds, at about 200 to about 450 degrees Celsius. The chamber pressure is preferably maintained at about 10 mTorr to about 100 mTorr, with the radio frequency (RF) power in the range of about 300 Watts to about 3000 Watts to ionize the hydrogen gas. The use of hydrogen plasma makes the low dielectric material softer and less chemical resistance, both undesirable characteristics.

[0015] The second step in the integrated method may be a microwave hydrogen plasma process. Microwave hydrogen plasma process 26 comprises subjecting low dielectric material 22 to a microwave hydrogen plasma in a chamber. The time duration for the exposure may range about 30 to about 600 seconds, and the wafer temperature may range about 200 to about 450 degrees Celsius. The chamber pressure may range about 10 mTorr to about 100 mTorr, with the microwave power in the range of about 300 Watts to about 3000 Watts.

[0016] Alternatively, the second step in integrated method 20 may be an electron beam treatment performed after the first step of hydrogen plasma process or concurrently therewith. Electron beam treatment comprises subjecting the material to a beam of electrons. Preferably, the electron beam is of an energy sufficient to penetrate the entire thickness of the low dielectric material. The low dielectric material may be exposed to an electron beam at an energy ranging from about 1 KeV to about 100 KeV (kilo-electron volt) for about 30 seconds to 600 seconds, for example. The wafer temperature may be held at about 200 to 450 degrees Celsius and the chamber pressure may be maintained at about 10 mTorr to 100 mTorr.

[0017] Another alternative for the second process in integrated method 20 is ultraviolet radiation. The ultraviolet radiation treatment may be performed after the first step of hydrogen plasma process or concurrently therewith. Ultraviolet radiation at about 1 mWatt to about 3000 Watts is applied to the low dielectric material for about 30 to 1200 seconds. The wafer temperature may be maintained at about 20 to 450 degrees Celsius and the chamber pressure may range about 10 mTorr to about 100 mTorr.

[0018] The result after the integrated treatment method 20 is a material that has a lower dielectric constant and has better mechanical stability that can withstand subsequent fabrication processes. More specifically, the resultant low dielectric material is harder and has better chemistry resistance. The integrated method also produces a better low dielectric material than any of the single treatment processes can provide.

[0019] FIGURE 3 is a simplified flowchart of an integrated post-deposition treatment method 30 according to another embodiment of the invention. A low dielectric material 32 such as a non-porogen material is first subjected to a thermal treatment process 34. For example, the wafer may be heated to about 200 to 450 degrees Celsius for about 10 minutes to about 12 hours in a chamber having a pressure at about 10 mTorr to about 760 Torr. The use of thermal post-deposition treatment makes the low dielectric material softer and less chemical resistance, both undesirable characteristics. Subsequently or concurrently therewith, a second treatment process 36 is applied. Second treatment process 36 of integrated method 30 comprises subjecting the low dielectric material to microwave hydrogen plasma. The time duration for the exposure may range about 30 to about 600 seconds, and the wafer temperature may range about 200 to about 450 degrees Celsius. The chamber pressure may range about 10 mTorr to about 100 mTorr, with the microwave power in the range of about 300 Watts to about 3000 Watts. The second processing step of microwave hydrogen plasma hardens the material in addition to lower the film shrinkage and stress problems. The result is a more robust and stable low dielectric material that has a lower dielectric constant.

[0020] Alternatively, the second post-deposition treatment process in method 30 may comprise electron beam applied subsequently to the thermal treatment or concurrently therewith. The low dielectric material may be exposed to an electron beam at an energy ranging from about

1 KeV to about 100 KeV for about 30 seconds to 600 seconds, for example. The wafer temperature may be held at about 200 to 450 degrees Celsius and the chamber pressure may be maintained at about 10 mTorr to 100 mTorr. Alternatively, ultraviolet radiation at about 1 mWatt to about 3000 Watts may be applied to the low dielectric material for about 30 to 1200 seconds as another alternate second treatment process in method 30. The wafer temperature may be maintained at about 20 to 450 degrees Celsius and the chamber pressure may be held at 10 mTorr to 100 mTorr. These second processing steps also improves the hardness and the cracking and stress problems of the low dielectric material.

[0021] The result after the integrated treatment method 30 is a material that has a lower dielectric constant and has better mechanical stability. This material is better able to withstand subsequent fabrication processes like chemical mechanical planarization. In particular, the resultant low dielectric material is harder and has better chemistry resistance. The integrated method also produces a better low dielectric material than any of the single treatment processes can provide.

[0022] FIGURE 4 is a simplified flowchart of an integrated post-deposition treatment method 40 according to another embodiment of the invention. Integrated method 40 may be used on a non-porogen low dielectric material 42. Integrated method 40 comprises a first step of applying an electron beam 44 to the low dielectric material. The electron beam-treated low dielectric material suffers from a low cracking threshold and high film stress. A second step performed subsequent to or concurrent with the first step may comprise treating the low dielectric material with a carbon-based plasma. The material is subjected to the carbon-based plasma for about 30 seconds to about 600 seconds in a chamber pressure of about 10 mTorr to about 100 mTorr. The wafer temperature may be held at about 200 to about 450 degrees Celsius. The RF power may range between about 300 Watts to about 3000 Watts. The resultant material has a lower dielectric constant and the problems of high film stress and low cracking threshold have been corrected by the use of the second step described above in combination with the first step of electron beam treatment.

[0023] FIGURE 5 is a simplified flowchart of an integrated post-deposition treatment method 50 according to another embodiment of the invention. Integrated method 50 first

subjects a low dielectric non-porogen material 52 to ultraviolet radiation. Ultraviolet radiation at about 1 mWatt to about 3000 Watts is applied to the low dielectric material for about 30 to 1200 seconds. The wafer temperature may be maintained at about 20 to 450 degrees Celsius and the chamber pressure may be held at 10 mTorr to 100 mTorr. The ultraviolet light-treated material generally has a low cracking threshold and high film stress. A second step performed subsequent to or concurrent with the first step may comprise treating the low dielectric material with a carbon-based plasma. The material is subjected to the carbon-based plasma for about 30 seconds to about 600 seconds in a chamber pressure of about 10 mTorr to about 100 mTorr. The wafer temperature may be held at about 200 to about 450 degrees Celsius. The RF power may range between about 300 Watts to about 3000 Watts. After the second step in the integrated method, the low cracking threshold and high film stress problems of the low dielectric have been corrected without increasing the dielectric constant of the material.

[0024] For porogen low dielectric materials, an embodiment of an integrated treatment method 60 is described in FIGURE 6. A porogen material contains pore generators that are capable of generating a porous network in the low dielectric material. Integrated treatment method 60 comprises a first step of treating the low dielectric material with electron beam, thermal, or microwave hydrogen plasma 62. This first step of integrated method 60 is operable to remove a significant number of the pore generators or porogens from the material. However, such processes generally introduce stress or cracks in the low dielectric material. Subsequently or concurrently therewith, a second step of applying a hydrogen-based or carbon-based plasma is used to “heal” the structure of the material. The second step is operable to generally relieve the crack and/or the stress in the low dielectric material. The result is a more mechanically stable material with a lower dielectric constant.

[0025] FIGURE 7 is a simplified cross-sectional view of an exemplary semiconductor device 70 having a substrate 72 and using an improved low dielectric material as inter-level dielectric layers 74 between conductive elements 76 such as metal interconnect lines. The improved low dielectric material is one which has been treated with the integrated processing method of the invention that comprises at least two curing steps as described in detail above. By using an integrated method comprising at least two processing steps to cure the low dielectric material, the structural integrity of the low dielectric material and thus the device is improved without

increasing the dielectric constant of the material. In most instances, the dielectric constant of the material is significantly lowered after the application of the integrated method processes.